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APPLICATION NO.	NO. FILING DATE			FIRST NAMED INVEN		ATTORNE	EY DOCKET NO.	CONFIRMATIO	N NO.		
10/761,239	10/761,239 01/22/2004		Jong-Hyun Choi			8947-000068/US 3822					
TITLE OF INVENTION	: SEMICONDUCTOR I	NTEGRATED (CIRCUIT			DUALIN		ATION S	SYSTEM	<u> </u>	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE I	DUE	PUBLICATION FEE	OUE	PREV. PAID ISSUE	FEE T	OTAL FEE(S) DUE	DATÉ D	JE	
nonprovisional	NO	\$1510		\$300		\$0		\$1810	05/11/20)09	
EXAMINER		ART UNI	Т	CLASS-SUBCLAS	s 95/12/2		000 DEMMANU2 00000029 1		761239		
ENGLUND, TERRY LEE		2816		327-374000		01 FC:1501 02 FC:1594			1510.00 OP 300.00 OP		
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	less an assignee is ident h in 37 CFR 3.11. Com						ee is ident	ified below, the do	cument has been	ı filed for	
(A) NAME OF ASSI		(B) RESIDENCE: (CITY and STATE OR COUNTRY)									
SUMSUNG ELECTRONICS CO., LTD. Gyeonggi-do, Republic of Korea									•		
Please check the appropr	iate assignee category or	r categories (will	not be pri	inted on the patent):		Individual Co	rporation	or other private gro	p entity Go	vernment	
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